

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,082,178 B2
APPLICATION NO. : 10/016915
DATED : July 25, 2006
INVENTOR(S) : David Meltzer

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page.

Item [54], change "LOCK DETECTOR CIRCUIT FOR DEJITTER PHASE LOCK LOOP (PLL)" to -- LOCK DETECTOR CIRCUIT FOR DEJITTER PHASE LOCK LOOP (PLL) APPLICATIONS--.

Column 10.

Line 19, change "said chase" to --said phase--.

Signed and Sealed this

Twelfth Day of December, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office